

200W SMD BI-DIRECTIONAL TVS FOR ESD PROTECTION DIODES, 5V

Dimensions (Inches / Millimeters):

- Top view: 0.035(0.90) / 0.028(0.70) (height), 0.051(1.30) / 0.043(1.10) (width), 0.014(0.35) / 0.009(0.25) (lead height)
- Side view: 0.028(0.70) / 0.020(0.50) (lead length)
- Bottom view: 0.067(1.70) / 0.059(1.50) (width), 0.007(0.20) / 0.002(0.05) (lead height)

PRODUCT FEATURES

1. FLAMMABILITY CLASSIFICATION 94V-0
2. LOW CLAMPING VOLTAGE, RESPONSE TIME <1nS TYP.
3. PROTECT ONE I/O LINE OR POWER LINE
4. IEC COMPATIBILITY:
 - IEC61000-4-2 (ESD) ±15KV (AIR), ±8KV (CONTACT)
 - IEC61000-4-4 (EFT) 40A (5/50nS)
 - IEC61000-4-5 (LIGHTNING) 18A (8/20μS)
5. LOW LEAKAGE CURRENT
6. CASE: TRANSFER MOLDED, SOD-523FL
7. DIMENSIONS IN INCHES AND (MILLIMETERS)
8. LEADS: SOLDERABILITY PER MIL-STD-750 METHOD 2026
9. WEIGHT: 0.002 GRAMS
10. RoHS COMPLIANT, ADD SUFFIX "H" FOR HALOGEN FREE
i.e. ESD5Z5.0C-H: RoHS COMPLIANT/HALOGEN FREE

ELECTRICAL CHARACTERISTICS

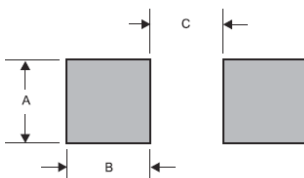
MAXIMUM RATINGS (T_A =25°C UNLESS OTHERWISE NOTED) AND ELECTRICAL CHARACTERISTICS

RATING	SYMBOL	UNITS
TOTAL POWER DISSIPATION, ON FR-5 PCB AT 25°C	P _D	150 mW
ESD VOLTAGE (HUMAN BODY MODEL)	E _{SD}	16 KV
THERMAL RESISTANCE	R _{θJA}	833 °C/W
STORAGE TEMPERATURE RANGE	T _{STG}	- 55 TO +150 °C
OPERATING JUNCTION TEMPERATURE RANGE	T _J	- 40 TO +125 °C

PART NUMBER	Max. V _{RWM} (V)	Max I _R @ V _{RWM} (μA)	Min V _{BR} @ I _T =1mA (A)	Max V _C @ I _{PP} =5A (V)	Max I _{PP} (A)	V _C @ Max I _{PP} (V) (NOTE 1)	MAX P _{PK} (W) (NOTE 1)	MAX C _J @ 1MHz, V _R =0V, (pF)	MARKING
ESD5Z5.0C	5	1	7.8	11.6	9.4	18.6	174	25	5C

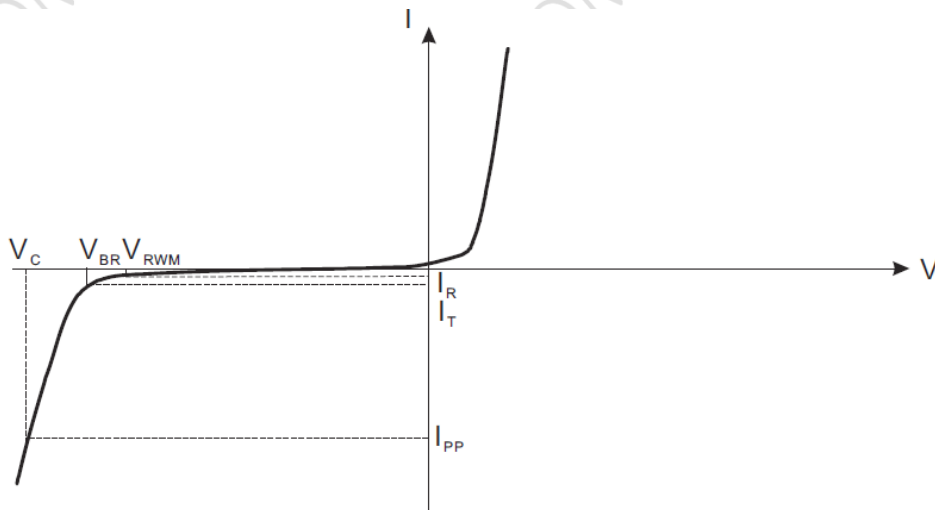
NOTE : 1. SURGE CURRENT WAVEFORM PER FIG 1.
2. V_{BR} IS MEASURED WITH A PULSE TEST CURRENT I_T AT 25°C AMBIENT TEMP

LAYOUT RECOMMENDATION



PACKAGE	A	B	C
SOD-523FL	0.032 (0.80)	0.024 (0.60)	0.044 (1.10)

RATINGS AND CHARACTERISTIC CURVES



Uni-Directional TVS

- V_C : Clamping Voltage @ I_{PP}
- I_{PP} : Maximum Reverse Peak Pulse Current
- V_{RWM} : Maximum Working Peak Reverse voltage
- I_R : Maximum Reverse Leakage Current @ V_{RWM}
- V_{BR} : Breakdown voltage @ I_T
- I_T : Test Current
- P_{PP} : Peak Pulse Power
- C_J : Max. Capacitance @ $V_R = 0V$ and $f = 1MHz$

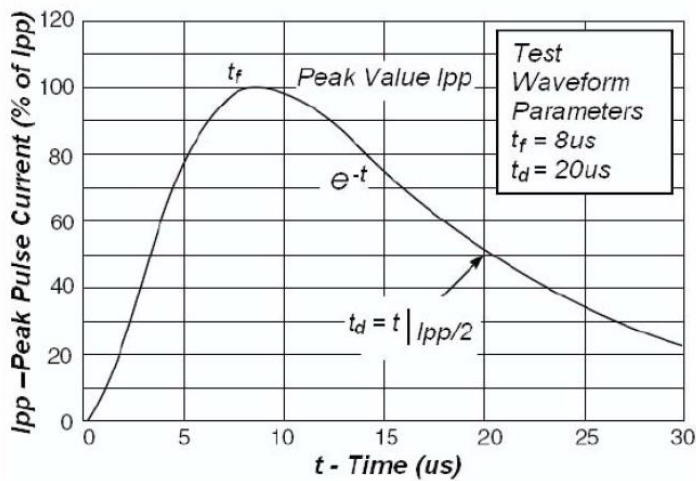


Fig1. Pulse Waveform

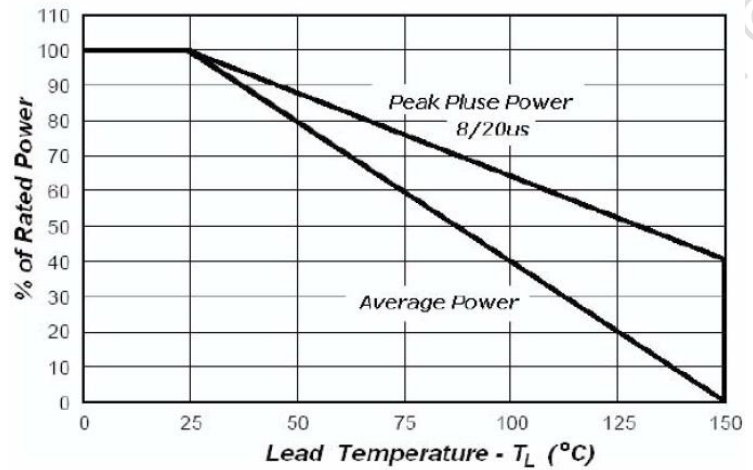


Fig2. Power Derating