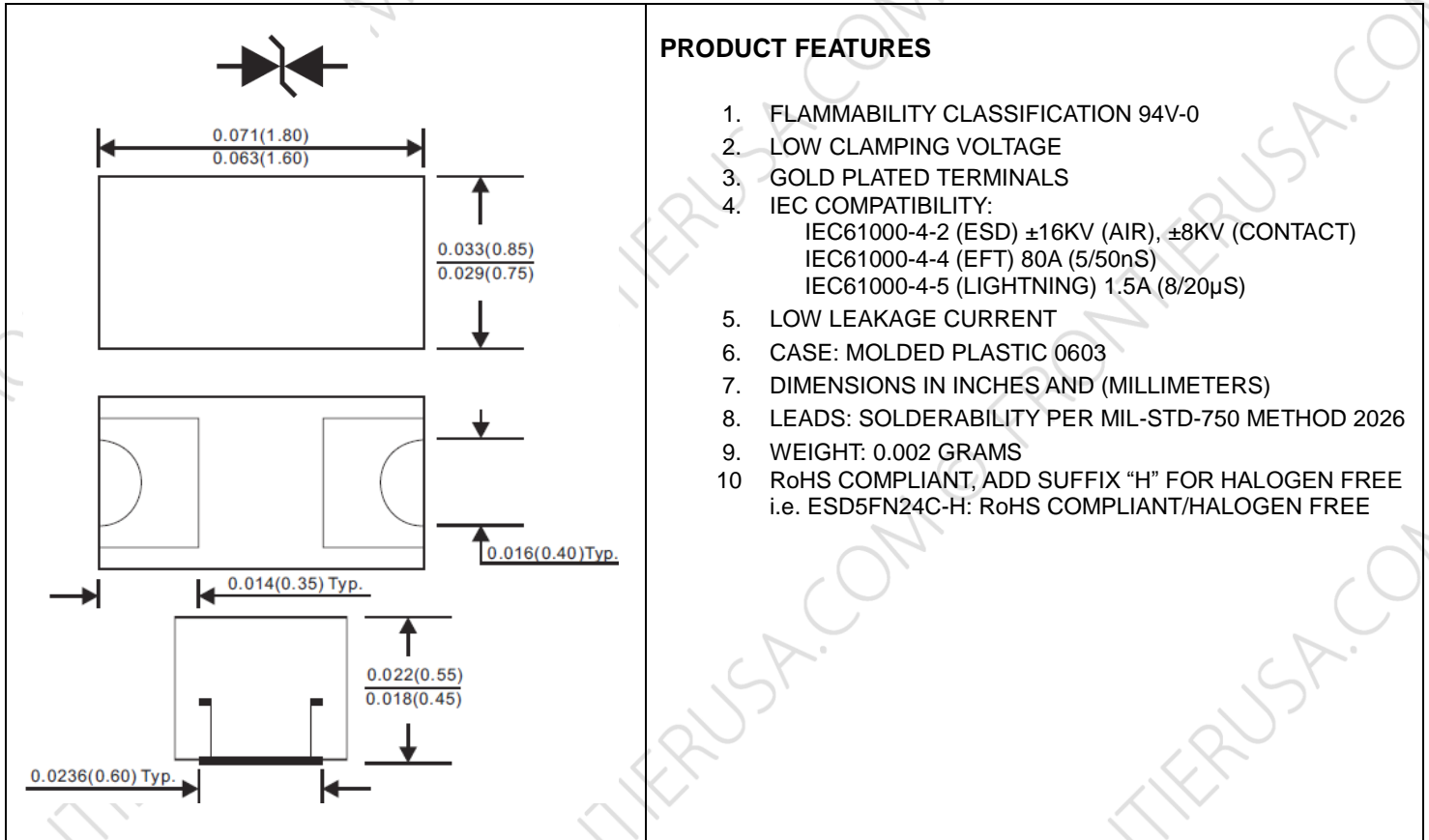


65W SMD BI-DIRECTIONAL TVS FOR ESD PROTECTION DIODES, 24V



ELECTRICAL CHARACTERISTICS

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED) AND ELECTRICAL CHARACTERISTICS

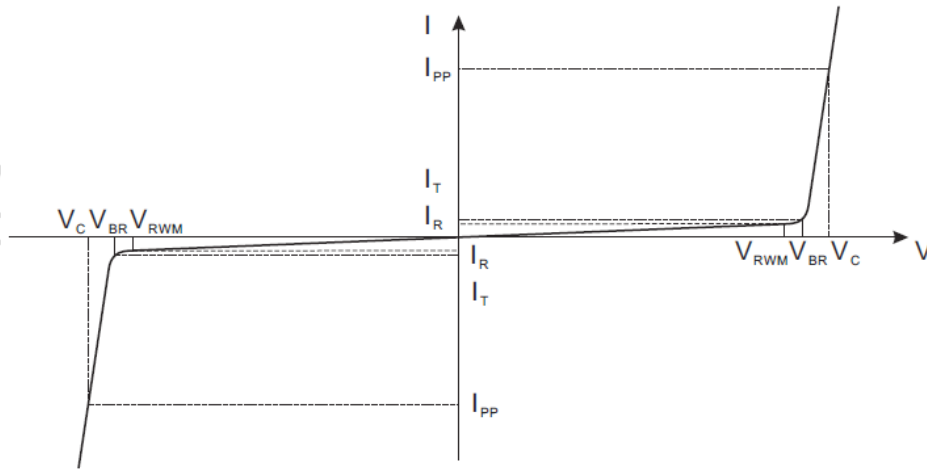
RATING	SYMBOL		UNITS
PEAK PULSE POWER, $t_p=8/20 \mu\text{S}$	PPP	65	W
STORAGE TEMPERATURE RANGE	T_{STG}	- 55 TO +150	$^\circ\text{C}$
OPERATING JUNCTION TEMPERATURE RANGE	T_J	- 55 TO +125	$^\circ\text{C}$

PART NUMBER	Max. V_{RWM} (V)	Max I_R @ V_{RWM} (μA)	Min V_{BR} @ $I_T=1\text{mA}$ (A)	Max V_C @ $I_{PP}=1.5\text{A}$ (V)	Max I_{PP} (A)	MAX C_J (pF)	MARKING
ESD5FN24C	24	1	25.5	43	1.5	18	X

NOTE : 1. SURGE CURRENT WAVEFORM PER FIG 1.
2. V_{BR} IS MEASURED AT AMBIENT TEMPERATURE OF 25°C .



RATINGS AND CHARACTERISTIC CURVES



Bi-Directional TVS

- V_C : Clamping Voltage @ I_{PP}
- I_{PP} : Maximum Reverse Peak Pulse Current
- V_{RWM} : Maximum Reverse Working voltage
- I_R : Maximum Reverse Leakage Current @ V_{RWM}
- V_{BR} : Breakdown voltage @ I_T
- I_T : Test Current
- P_{PP} : Peak Pulse Power
- C_J : Max. Capacitance @ $V_r = 0V$ and $f = 1MHz$

FIG.1- 8 X 20us PULSE WAVEFORM

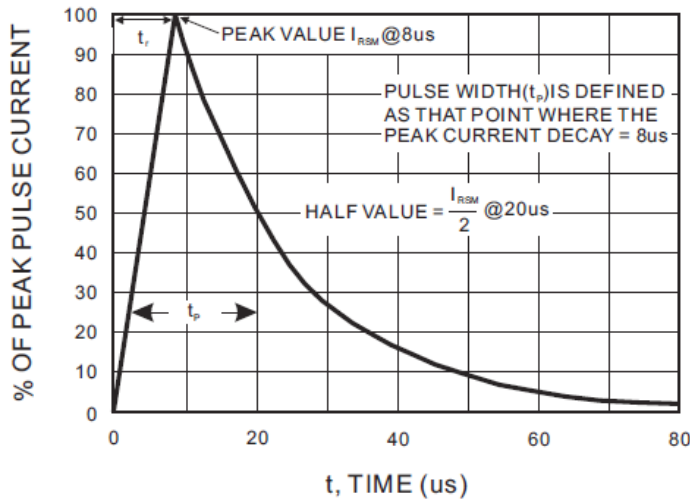
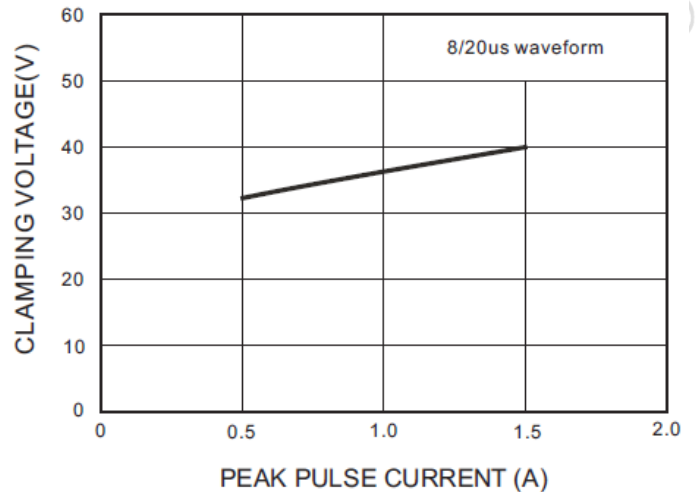
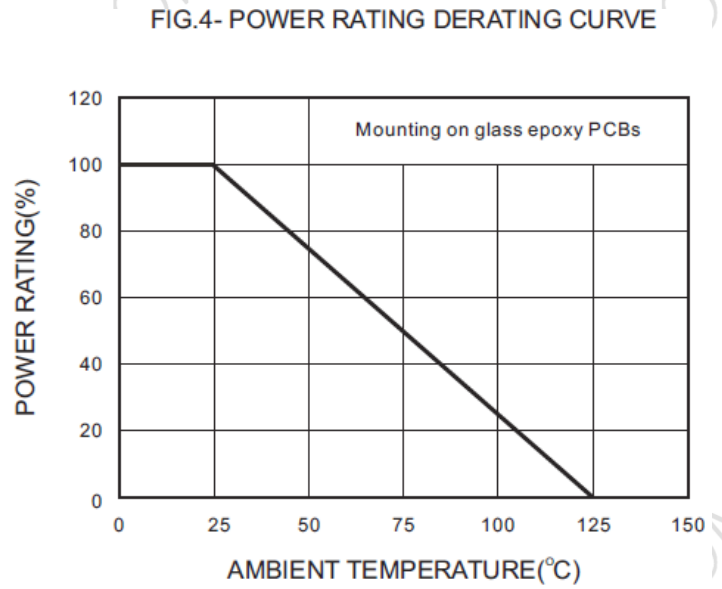
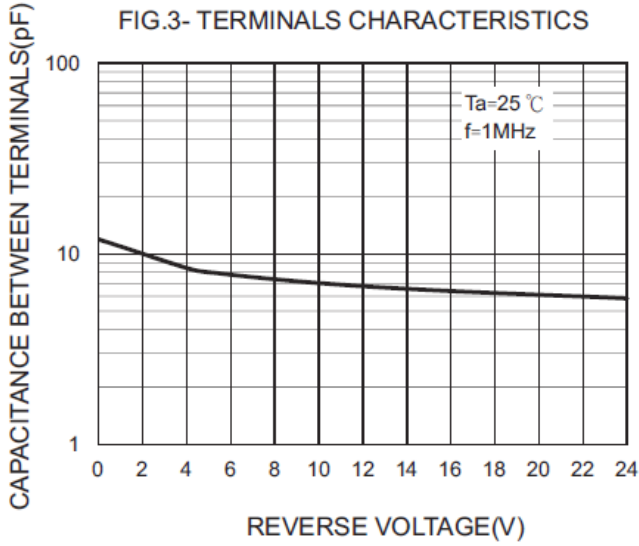
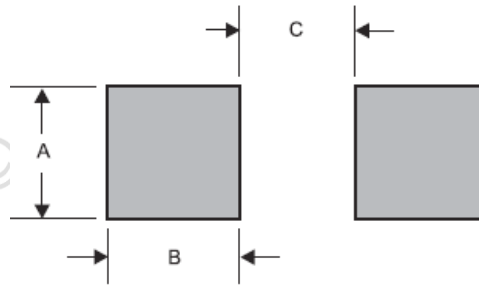


FIG.2- CLAMPING VOLTAGE VS. PEAK PULSE CURRENT





LAYOUT RECOMMENDATION



PACKAGE	A	B	C
0603	0.032 (0.80)	0.028 (0.70)	0.036 (0.90)